



Project: The Procurement of Supply and Delivery of Desktop Computer (Branded)- 142 Sets

IFB No.: NRB/KHARID/GOODS/01/083/84

Pre-Bid Queries Clarification

SN	Page No.	Section/ Clause	Description in Bid Document	Queries	Clarification
1	58	Section V. Schedule of Requirements	2. Detailed Technical Specifications and Standards.	Core count and cache specification: The Technical Specifications require a processor with "10 Core" and "Cache Memory (L3): 18 MB," with an "Efficiency core base frequency of 1.8 GHz." We note that this exact combination of L3 cache size and efficiency-core base frequency corresponds closely to a specific processor SKU, whose published core count is 14, not 10. Could the Purchaser kindly confirm: a. Whether "10 Core" is the intended requirement, or whether this is a typographical error; and b. Whether the core count, cache size, and efficiency-core base frequency are independent minimum requirements, or whether they are intended to describe one specific processor model?	1.a. It is the intended requirement. 1.b. Core count, cache size and efficiency-core base frequency are independent. These are minimum requirements. One can propose equal or higher.
2	58	Section V. Schedule of Requirements	2. Detailed Technical Specifications and Standards.	Display size: The specification calls for a "23.0" LED FHD" display. As the majority of commercially available branded All-in-One desktop displays in this class are manufactured at 23.8", we request confirmation of whether a 23.8" display would be considered compliant, or whether 23.0" is a strict requirement.	2. This is the minimum requirement; bigger Display Size (23.8") is acceptable.



3	15	Section I. Instructions to Bidders (ITB)	18. Documents Establishing the Conformity of the Goods and Related Services to the Bidding Document	Brand-neutrality of specifications: Per ITB Clause 18.4 and GCC Clause 21.1, technical specifications are intended to be descriptive rather than restrictive, allowing bidders to offer other standards, brands, or models demonstrating substantial equivalence. We would be grateful if the Purchaser could confirm that the technical specifications as drafted are not intended to restrict compliance to a single manufacturer or product line, and that bids offering functionally equivalent or superior alternatives meeting the stated performance benchmarks (regardless of exact core count, cache size, or display size) will be evaluated as substantially responsive.	3. This specification has been developed as per the need of the Bank and is not specific to any particular brand and model. If the Brand/Model fulfills the minimum requirement as per the technical specification is eligible for the bidding.
4	53	43 Section IV. Bidding Forms	Manufacturer's Authorization Letter	We would like to respectfully request clarification regarding the Manufacturer's Authorization Form (MAF) for the above-mentioned procurement. Due to some unavoidable circumstances, we were unable to attend today's pre-bid meeting. However, we have received information that the OEM has indicated that the MAF for this project may be issued on an exclusive basis. As we understand that the MAF is being considered exclusive for this project, and that the MAF may therefore not be issued to all eligible partners, we kindly request Nepal Rastra Bank to clarify whether the requirement for this procurement is indeed intended to be exclusive to a particular bidder/partner or whether the OEM is permitted to issue MAF to multiple eligible bidders.	4. MAF issued by the OEM to multiple eligible bidders shall be acceptable.
5	53	43 Section IV. Bidding Forms	Manufacturer's Authorization Letter	We have been informed by the OEM that the MAF for this project is exclusive and will not be provided to all partners. Therefore, we kindly request Nepal Rastra Bank to clarify whether the MAF for this project is exclusive or not.	5. MAF issued by the OEM to multiple eligible bidders shall be acceptable.



6	58	Section V. Schedule of Requirem ents	2. Detailed Technical Specifications and Standards.	On Page 58 - Technical Specifications Line item 5 "Cache Memory (L3)", please be informed that Intel Processors nowadays do not specify L3 Cache. It only mentions L2 Cache Or Intel Smart Cache. So if you meant L2 Cache, please amend it accordingly OR if you meant L3 cache please accept an alternate definition "Intel Smart Cache" so that there is no confusion.	6. In Line item 5 "Cache Memory (L3)" the alternate definition Intel Smart Cache is acceptable.
7	59	Section V. Schedule of Requirem ents	2. Detailed Technical Specifications and Standards.	On Page 59 - Line item 13 "I/O Ports" - as discussed during the pre-bid meeting, we will provide a DP-Port Adaptor for the computer model not having DP Port.	7. External DB Port Adaptor is acceptable.
8	37	Section II. Bid Data Sheet	ITB 12.1 (h)	On Page 37, Bid Data Sheet Item 12.1H - for the documents referenced in "(iii) Warranty Certificate/Letter Affirming Warranty and (iv) Declaration of Spare Parts Availability for 5 Years", these will be provided by the bidder (authorized reseller) itself and not by the OEM. Please confirm your acceptance of this.	8. We intend that bidder should make a confirmation to provide the spare parts for 5 years.
Intel® 13th & 14th Generation Core™ Processor Datasheet – Smart Cache Technology					